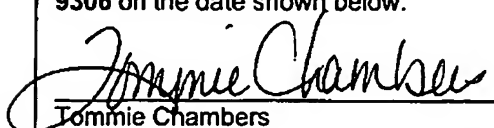


To: Techonology Unit: 2829
Facsimile Number: 703-872-9306

Total Pages Sent 3 SEP 16 2004

From: W. Daniel Swayze, Jr.
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 972-917-5633

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATION OF FACSIMILE TRANSMISSION	
I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:	
 Tommie Chambers	9-16-04 Date

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NAME OF INVENTOR(S): Potts		RECEIPT DATE & SERIAL NO.:	
TITLE OF INVENTION: SEMICONDUCTOR WAFER WITH GROUPE INTEGRATED CIRCUIT DIE HAVING INTER-DIE CONNECTIONS FOR GROUP TESTING		Serial No.: 10/055,445 Filing Date: 1/23/2002	
TI FILE NO.:	DEPOSIT ACCT. NO.:		
TI-27832	20-0668		
FAXED: 9-16-04 DUE: 09/15/2004 ATTY/SECY: WDS/tic			

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265

SEP 16 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Potts
Serial No: 10/055,445
Filed: 1/23/2002
For: SEMICONDUCTOR WAFER WITH GROUPED INTEGRATED CIRCUIT
DIE HAVING INTER-DIE CONNECTIONS FOR GROUP TESTING

Docket No: TI-27832
Examiner: Geyer, Scott B.
Art Unit: 2829

TRANSMITTAL LETTER

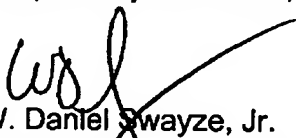
Assistant Commissioner For Patents
Washington, DC 20231

Dear Sir:

Enclosed is a Notice of Appeal. Applicants have filed this Notice of Appeal in order for the Examiner to review the Response filed on August 20, 2004. Applicants believe that the application is in condition for allowance. However, Applicants respectfully request the Examiner to provide a positive indication of allowability of the instant application.

The Commissioner is authorized to charge any fees required to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,


W. Daniel Swayze, Jr.
Attorney for Applicants
Reg. No. 34,478

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5633

TI-27832

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Office at 703-872-9306 on the date shown below:


Tommie Chambers

9-16-04
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Potts, et al. Docket No: TI-27832
Serial No: 10/055,445 Examiner: Geyer, Scott B.
Filed: 1/23/2002 Art Unit: 2829
For: SEMICONDUCTOR WAFER WITH GROUPED INTEGRATED CIRCUIT DIE HAVING
INTER-DIE CONNECTIONS FOR GROUP TESTING

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION I hereby
certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 703-872-
9306 on the date shown below:

Tommie Chambers
Tommie Chambers
9-16-04

Applicant hereby appeals to the Board of Appeals from the decision mailed June 15, 2004, of the
Primary Examiner finally rejecting Claims 1-11, 22, and 23.

The item(s) checked below are appropriate:

1. ☒ An extension of time to respond to the final rejection
was granted on _____.
☒ is requested for 1 month.
2. ☐ A timely response to the final rejection has been filed, as provided in
841 O.G. 1411.
3. ☒ Fee \$330.00:
☐ Not required (Fee paid in prior appeal)
☒ The Commissioner of Patents is hereby authorized to charge any
fees which may be required or credit any overpayment to the deposit
account of Texas Instruments Incorporated, Account No. 20-0668.
This form is submitted in duplicate.

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5633

W. Daniel Swayze, Jr.
W. Daniel Swayze, Jr.
Attorney for Appellant
Reg. No. 34,478